

In re the Application of:

Group Art Unit: 2825

Examiner: Whitmore, Stacy

Confirmation No.: 5787

**For: METHOD AND SYSTEM FOR
FACILITATING ELECTRONIC CIRCUIT
AND CHIP DESIGN USING REMOTELY
LOCATED RESOURCES**

DO NOT
ENTER
NW

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the final Office Action mailed June 07, 2007, Applicants respectfully request to amend the application as follows:

Remarks begin on page 11: